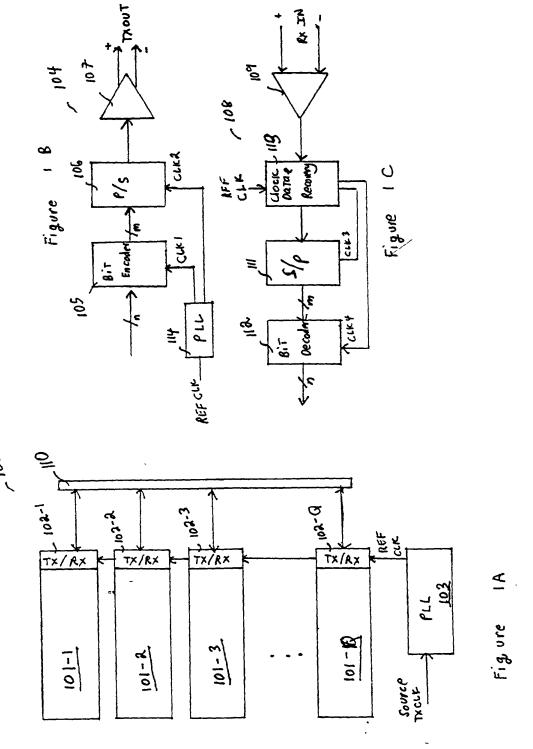
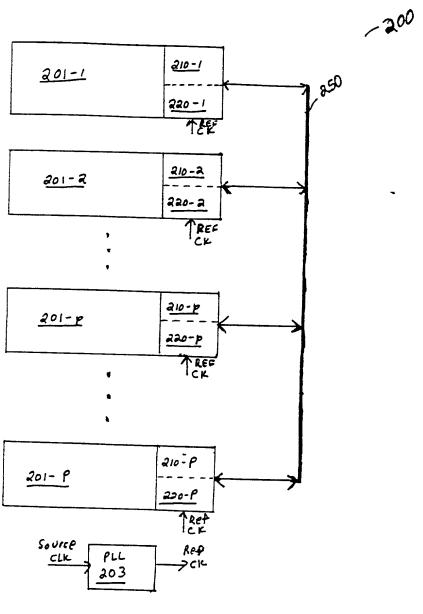
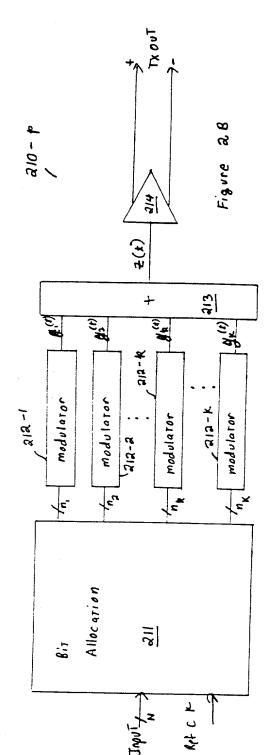
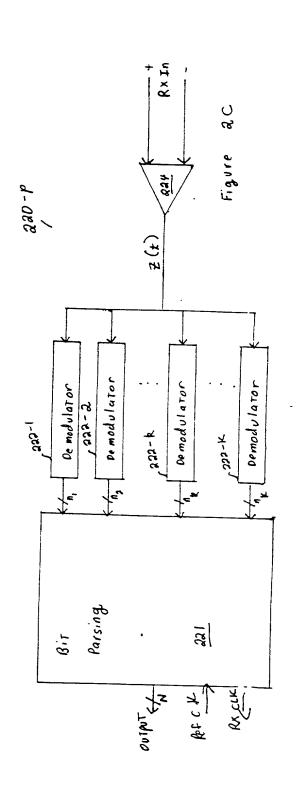
Title: "Low Complexity High-Speed ..." Inventor(s): Sreen Raghavan M-12165 US



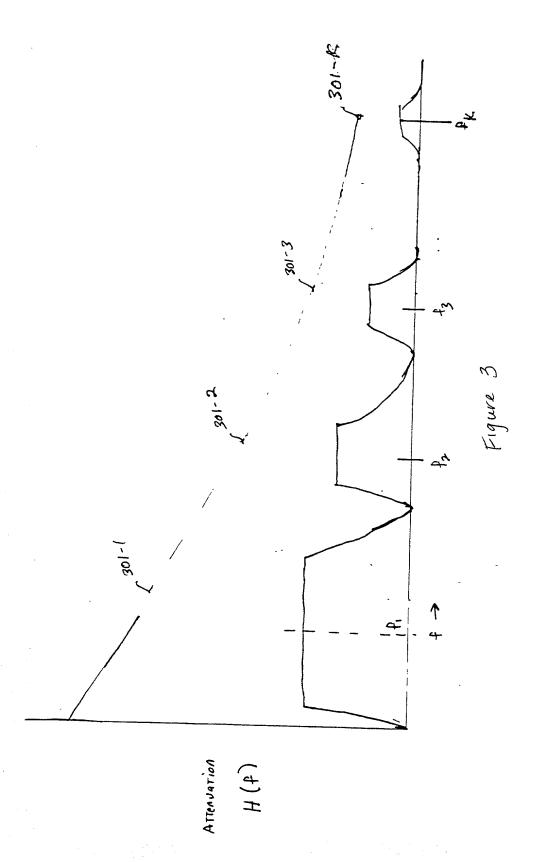
DSG65045 OSE51







Title: "Low Complexity High-Speed ..." Inventor(s): Sreen Raghavan M-12165 US



Title: "Low Complexity High-Speed ..."
Inventor(s): Sreen Raghavan
M-12165 US 42 410 COS(RT+4) Sin (alltat) £ £ → cos (294 4x) CK DAC DAC 7 T 906 403 Symbol 402 Encoder 770 기는 CE Scrambler 19 \$ 7 5

Title: "Low Complexity High-Speed ..." Inventor(s): Sreen Raghavan M-12165 US S Jdp0200 5:11 81 I OFFSET and 513 and. Eguali zer 513 90° 130 <u>୍ଟିଫ୍ଲ</u> OFF SET, OFFEE Jay - 20 t 3 S S 2/2-4-AGC 8020 Sig (all fr t) 20

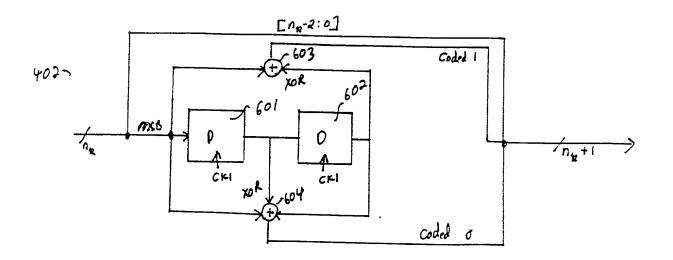


Figure GA

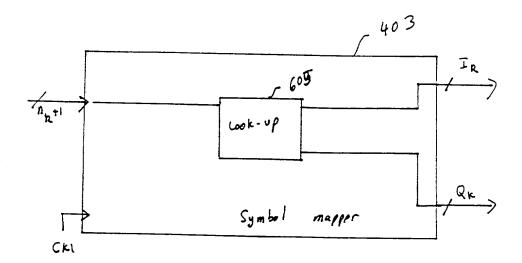


Figure 6B

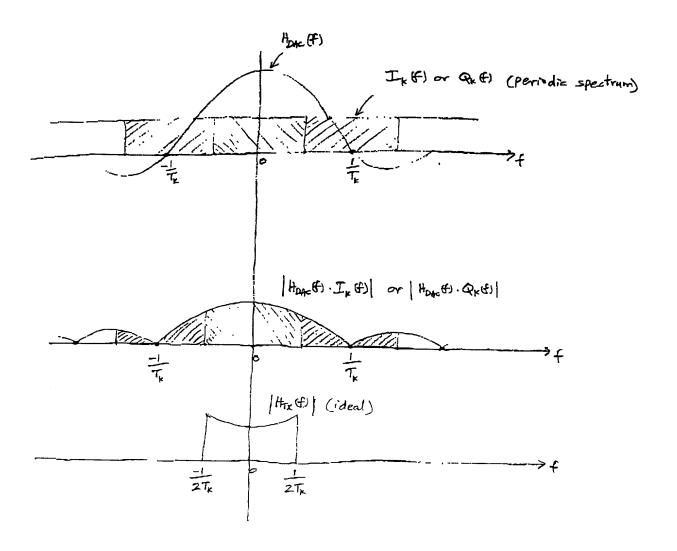


Figure 60

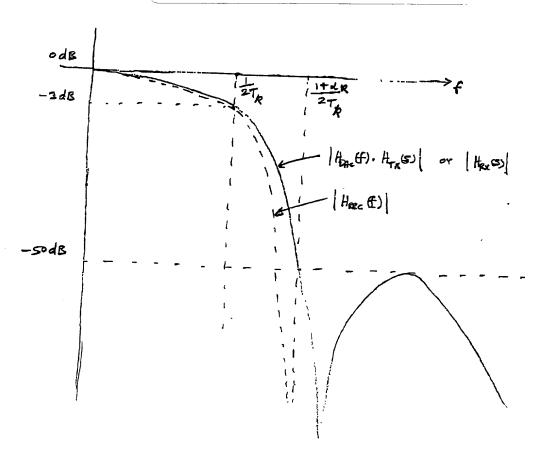


Figure BE

Figure 6c

Title: "Low Complexity High-Speed ..." Inventor(s): Sreen Raghavan M-12165 ÚS Do(a) OF(a) CH (n+1) CK (n+1) 702 Coefficient UPDATE GRAD GRAD AT (A) AR(A) 703 407 PR Phase Loop LR To(nti) 6 Filter DETECTOR Error -701 Calculation 708 706 LOOP Phase Skint) Filter DeTector 704 722 720 Phase e (nti) Denecto! 724 723 [FA (A)] -3(n11) E 1FR(n) 726 725 FEG) 728 127 -ep(n) Figure 7 Phase perector 731 729 734 Phase Оетесто à con

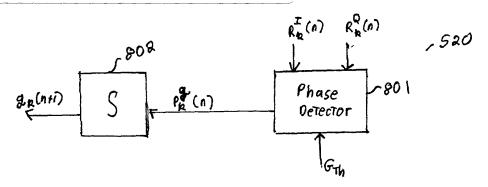


Figure 8A

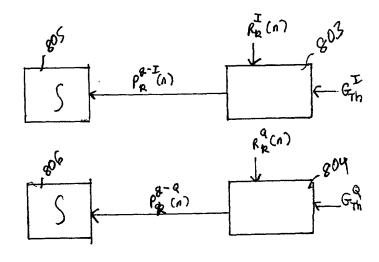


Figure 8B

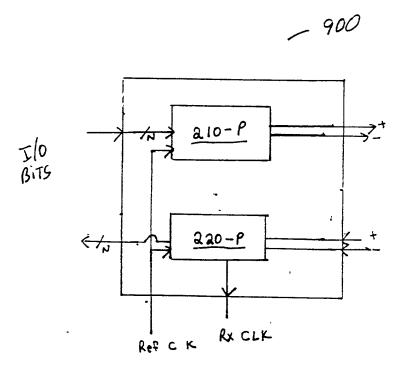


Figure 9

